What is claimed is:

1. An apparatus comprising:

an input/output (I/O) device;

said I/O device being operative to receive a fragment of electronic data, and further being operative to identify at least a portion of the contents of said fragment of electronic data, and further being operative to moderate one or more interrupts of an associated computing platform processor, based at least in part on the at least a portion of said contents.

2. The apparatus of claim 1, wherein the at least a portion of said contents comprises an acknowledgement (ACK).

3. The apparatus of claim 1, wherein said I/O device comprises a network interface card (NIC).

4. The apparatus of claim 1, wherein at least a portion of said contents comprises a priority designation.

5. The apparatus of claim 1, wherein said I/O device is configured to moderate by substantially immediately asserting said one or more interrupts of said associated computing platform processor.

6. The apparatus of claim 1, wherein said I/O device is configured to moderate by deferring said one or more interrupts of said associated computing platform processor so that a predetermined number of interrupts per unit of time is not exceeded.

7. The apparatus of claim 1, wherein said I/O device is configured to moderate by deferring said one or more interrupts until a particular number of fragments of electronic data of a particular type are received by said I/O device.

8. The apparatus of claim 1, wherein said I/O device is configured to moderate by deferring said one or more interrupts until a particular quantity of electronic data is received.

9. The apparatus of claim 1, wherein said moderation of associated computing platform interrupt scheme is configurable through a user interface.

10. The apparatus of claim 1, and further comprising:

said I/O device further being operative to measure a particular period of time after the receipt of a fragment of electronic data, and further being operative to moderate one or more interrupts of an associated computing platform after said particular period of time has elapsed.

11. A method of moderating one or more interrupts of an associated computing platform comprising:

receiving a fragment of electronic data;

identifying, at least partially, the contents of said fragment of electronic data; and

moderating said one or more interrupts based at least in part on said at least partially identified contents.

12. The method of claim 11, wherein said at least partially identified contents comprises an acknowledgement (ACK).

13. The method of claim 11, wherein said at least partially identified contents comprises a priority designation.

14. The method of claim 11, wherein said moderating comprises substantially immediately interrupting said associated computing platform processor.

15. The method of claim 11, wherein said moderating comprises deferring said one or more interrupts of said associated computing platform processor if a predetermined number of interrupts per unit time is met or exceeded.

16. The method of claim 11, wherein said moderating comprises deferring said one or more interrupts until a particular number of fragments of electronic data of a particular type are received.

17. The method of claim 11, wherein said moderating comprises deferring said one or more interrupts until a particular quantity of electronic data is received.

18. The method of claim 11, wherein said moderating is configurable through a user interface.

19. The method of claim 11, and further comprising:

measuring a particular period of time after the receipt of a fragment of electronic data; and

performing said moderating after said particular period of time has elapsed.

20. An article comprising:

a storage medium;

said storage medium having stored thereon instructions, that when executed by a computing platform, result in execution of a method of processing latency sensitive electronic data comprising:

receiving a fragment of electronic data;

at least partially identifying the contents of at least a portion of said fragment of electronic data; and

moderating said one or more interrupts based at least in part on said at least partially identified contents.

21. The article of claim 20, wherein said at least partially identified contents comprises an acknowledgement (ACK).

22. The article of claim 20, wherein said at least partially identified contents comprises a priority designation.

23. The article of claim 20, wherein said moderating comprises substantially immediately interrupting said associated computing platform processor.

24. The article of claim 20, wherein said moderating comprises deferring said interrupting of said associated computing platform processor.

25. The article of claim 20, wherein said moderating comprises deferring said one or more interrupts until a particular number of fragments of electronic data of a particular type are received.

26. The article of claim 20, wherein said moderating comprises deferring said one or more interrupts until a particular quantity of electronic data is received.

27. The article of claim 20, wherein said moderating is configurable through a user interface.

28. The article of claim 20, and further comprising:

measuring a particular period of time after the receipt of a fragment of electronic data; and performing said moderating after said particular period of time has elapsed.